



DSS5240Y

40V LOW V_{CE(sat)} PNP SURFACE MOUNT TRANSISTOR

Features

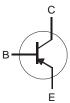
- Epitaxial Planar Die Construction
- Ideal for Low Power Amplification and Switching
- Ultra Small Surface Mount Package
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free, "Green Device" (Note 2)
- ESD rating: 400V-MM, 8KV-HBM

Mechanical Data

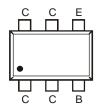
- Case: SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper Plated Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.006 grams (approximate)



Top View



Device Symbol



Top View Pin Out Configuration

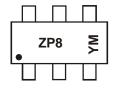
Ordering Information (Note 3)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DSS5240Y-7	ZP8	7	8mm	3,000

Notes:

- 1. No purposefully added lead.
- 2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com
- 3. For packaging details, go to our website at http://www.diodes.com

Marking Information



ZP8 = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: W = 2009)

M = Month (ex: 9 = September)

Date Code Key

Year	2009		2010	2011		2012	2013		2014	2015		2016
Code	W		Χ	Y		Z	Α		В	С		D
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-40	V
Collector-Emitter Voltage	V _{CEO}	-40	V
Emitter-Base Voltage	V _{EBO}	-5	V
Collector Current - Continuous	Ic	-2	Α
Peak Pulse Collector Current	I _{CM}	-3	Α
Base Current (DC)	I _B	-300	mA
Peak Base Current	I _{BM}	-1	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4) @ T _A = 25°C	P_{D}	625	mW
Thermal Resistance, Junction to Ambient (Note 4) @ T _A = 25°C	$R_{ hetaJA}$	200	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

4. Device mounted on FR-4 PCB, with minimum recommended pad layout.

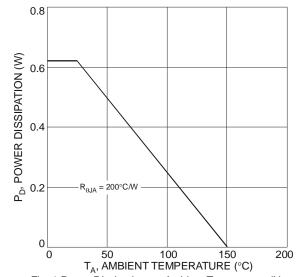
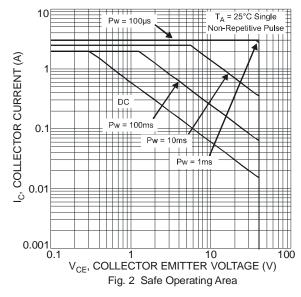


Fig. 1 Power Dissipation vs. Ambient Temperature (Note 4)



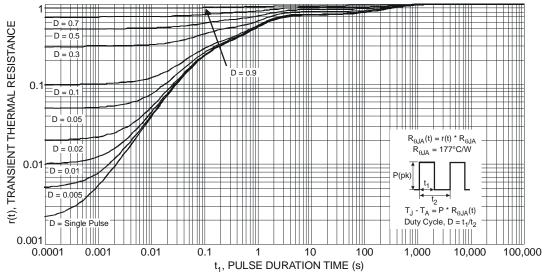


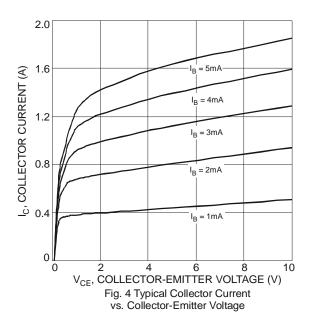
Fig. 3 Transient Thermal Response

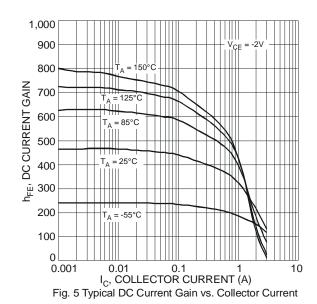


Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	-40	_		V	$I_C = -100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 5)	BV_{CEO}	-40	_		V	$I_C = -10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV _{EBO}	-5	_		V	$I_E = -100 \mu A, I_C = 0$
Collector Cutoff Current	I _{CBO}	_	_	-100	nA	$V_{CB} = -30V, I_{E} = 0$
Concetor Cuton Current	ICBO			-50	μΑ	$V_{CB} = -30V, I_E = 0, T_A = 150$ °C
Emitter Cutoff Current	I _{EBO}		_	-100	nA	$V_{EB} = -4V, I_C = 0$
		300	450	_		$V_{CE} = -2V, I_{C} = -100mA$
DC Current Gain (Note 5)	h _{FE}	260	380	_		$V_{CE} = -2V, I_{C} = -500mA$
Do Garrett Gair (Note 3)	IIFE	210	325	_		$V_{CE} = -2V, I_{C} = -1A$
		100	210			$V_{CE} = -2V, I_{C} = -2A$
			_	-100		$I_C = -100 \text{mA}, I_B = -1 \text{mA}$
	V _{CE(sat)}	_	_	-110	mV	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Collector-Emitter Saturation Voltage (Note 5)		_	_	-225		$I_C = -750 \text{mA}, I_B = -15 \text{mA}$
		_		-225		$I_C = -1A$, $I_B = -50mA$
		_		-350		$I_C = -2A$, $I_B = -200mA$
Collector-Emitter Saturation Resistance	R _{CE(sat)}	_		-220	mΩ	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Base-Emitter Saturation Voltage (Note 5)	V _{BE(sat)}	_	-1.0	-1.1	٧	$I_C = -2A$, $I_B = -200mA$
Base-Emitter Turn On Voltage (Note 5)	$V_{BE(on)}$	_	-0.67	-0.75	>	$V_{CE} = -2V, I_{C} = -100mA$
Output Capacitance	C_{obo}	_	25	40	рF	$V_{CB} = -10V, f = 1.0MHz$
Current Gain-Bandwidth Product	f_T	100	220		MHz	$V_{CE} = -10V$, $I_{C} = -50mA$, $f = 100MHz$
Turn-On Time	t _{on}	_	73		ns	
Delay Time	t _d	_	27		ns	
Rise Time	t _r	_	46	_	ns	V _{CC} = -10V
Turn-Off Time	t _{off}	_	237	_	ns	$I_C = -1A$, $I_{B1} = I_{B2} = -50$ mA
Storage Time	ts	_	195		ns	
Fall Time	t _f		42		ns	

Notes: 5. Measured under pulsed conditions. Pulse width = $300\mu s$. Duty cycle $\leq 2\%$.







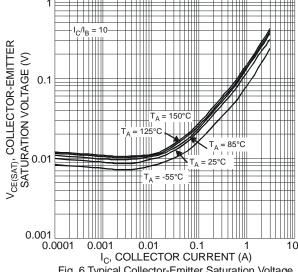
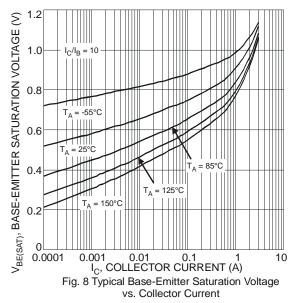


Fig. 6 Typical Collector-Emitter Saturation Voltage vs. Collector Current



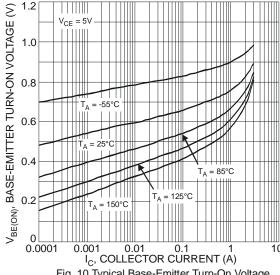


Fig. 10 Typical Base-Emitter Turn-On Voltage vs. Collector Current

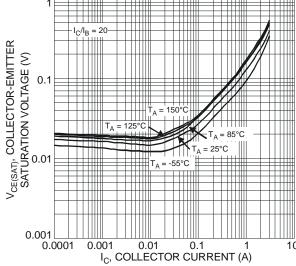


Fig. 7 Typical Collector-Emitter Saturation Voltage vs. Collector Current

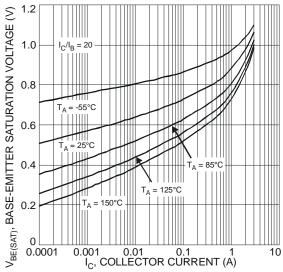
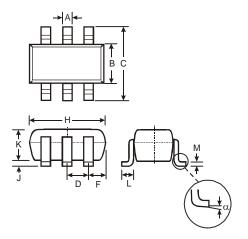


Fig. 9 Typical Base-Emitter Saturation Voltage vs. Collector Current

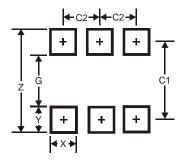


Package Outline Dimensions



SOT-363					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
С	2.00	2.20			
D	0.65 Typ				
F	0.40	0.45			
Н	1.80 2.20				
J	0 0.10				
K	0.90 1.00				
L	0.25 0.40				
M	0.10	0.22			
α	0°	8°			
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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