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FAIRCHILD

SEMICONDUCTOR®

BD176/178/180

Medium Power Linear and Switching Applications

Complement to BD 175/177/179 respectively



BD176/178/180

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parame	eter	Value	Units
V _{CBO}	*Collector-Base Voltage	: BD176	- 45	V
020	_	: BD178	- 60	V
		: BD180	- 80	V
V _{CEO}	Collector-Emitter Voltage	: BD176	- 45	V
010		: BD178	- 60	V
		: BD180	- 80	V
V _{EBO}	Emitter-Base Voltage		- 5	V
I _C	Collector Current (DC)		- 3	А
I _C	*Collector Current (Pulse)		- 7	А
P _C	Collector Dissipation (T _C =25°C)		30	W
R _{θja}	Junction to Ambient		70	°C/W
R _{θjc}	Junction to Case		8.5	°C/W
TJ	Junction Temperature		150	°C
T _{STG}	Storage Temperature		- 65 ~ 150	°C

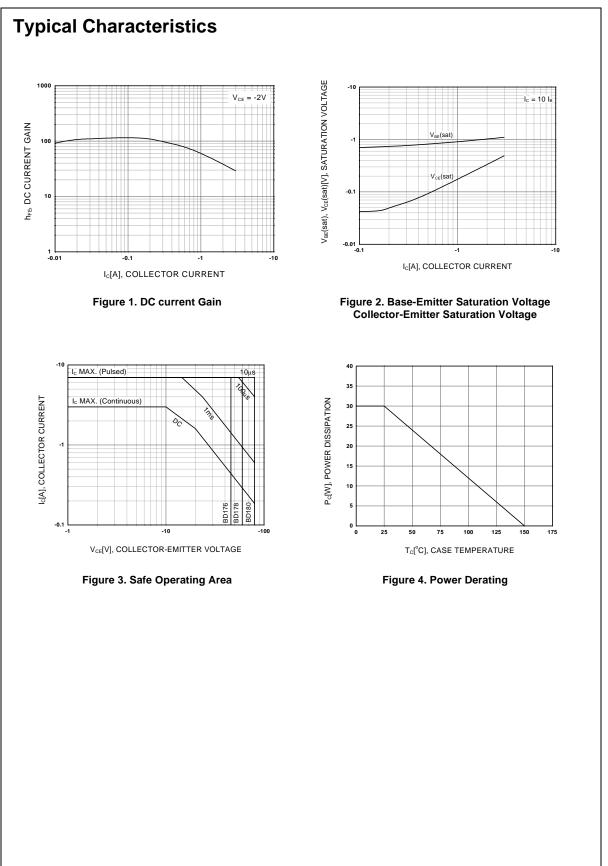
Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining Voltage					
	: BD176	I _C = - 100mA, I _B = 0	- 45			V
	: BD178		- 60			V
	: BD180		- 80			V
I _{CBO}	Collector Cut-off Current : BD176	$V_{CB} = -45V, I_E = 0$			- 100	μΑ
	: BD178	$V_{CB} = -60V, I_E = 0$			- 100	μΑ
	: BD180	$V_{CB} = -80V, I_E = 0$			- 100	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			- 1	mA
h _{FF1}	* DC Current Gain	V _{CE} = - 2V, I _C = - 150mA	40		250	
h _{FE2}		$V_{CE} = -2V, I_{C} = -1A$	15			
V _{CE} (sat)	* Collector-Emitter Saturation Voltage	I _C = -1 A , I _B = - 0.1A			- 0.8	V
V _{BE} (on)	* Base-Emitter On Voltage	V _{CE} = - 2V, I _C = -1 A			- 1.3	V
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -250mA$	3			MHz

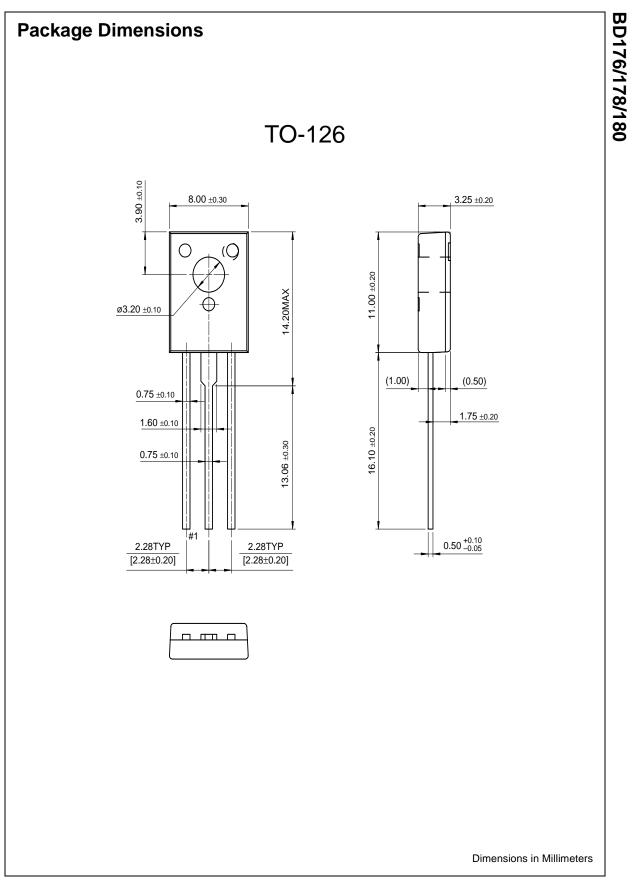
h_{FE} Classificntion

Classification	6	10	16
h _{FE1}	40 ~ 100	63 ~ 160	100 ~ 250
* Classification 16: Only BD 176	-	•	

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BD176/178/180



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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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